

## **CURRICULUM VITAE**

**Dr. Daryoosh Dideban, Associate professor**

### **Permanent Address:**

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### **Academic Qualifications**

1998 B.Sc.in Electrical Engineering(Electronics), [Isfahan University of Technology](http://www.isfahan.ac.ir), Isfahan, Iran

2000 M.Sc in Electrical Engineering(Electronics), [Sharif University of Technology](http://www.sharif.ac.ir), Tehran, Iran

2012 PhD in Electrical and Electronic Engineering (nano-Electronics), [University of Glasgow](http://www.glasgow.ac.uk), Glasgow, United Kingdom

### **Academic Distinctions, Employment and Scholarships**

1994 Acceptance in Iran National University Entrance Exam (Konkour) with rank of 70.

1998 B.Sc in Electrical Engineering (Electronics) from IUT with honors degree, grade A.

2001 M.Sc in Electrical Engineering (Electronics) from SUT with honors degree, grade A.

2001 Appointed as a member of academic staff, University of Kashan, Iran.

2003 Employment as “Tenure Instructor” after scientific confirmation of examiner board.

2008 Awarded full scholarship from Ministry of Science, Research and Technology

2012 Awarded Ph.D. from University of Glasgow (with honors)

2018 Promoted from Assistant professor to Associate professor.

2011-2020 Reviewer of more than 100 scientific papers in reputable journals.

2012-2020 Supervisor of 20 graduated M.Sc. students and 8 graduated Ph.D. students.

2018-2020 Supervisor of one Post-doc researcher at Institute of nanoscience and nanotechnology

## Teaching Experience

### Courses

- *Semiconductor Devices 1,2 (Physics of Electronics 1,2)*
- *Nano-Electronics (and Special topics in nano-Electronics)*
- *Advanced Electronic Circuit Design*
- *Digital Electronics, Electronics 1,2,3*

## Publications

For a list of recent and complete publications please see:

[https://scholar.google.com/citations?hl=en&user=gxHYyzMAAAAJ&view\\_op=list\\_works&sortby=pubdate](https://scholar.google.com/citations?hl=en&user=gxHYyzMAAAAJ&view_op=list_works&sortby=pubdate)

or

[https://www.researchgate.net/profile/Daryoosh\\_Dideban](https://www.researchgate.net/profile/Daryoosh_Dideban)

## Theses, Grants and Research Projects

- 1- D. Dideban, "**Statistical Modeling of Nano CMOS Transistors with Surface Potential Compact Model PSP**", PhD dissertation, University of Glasgow, United Kingdom, 2012.
- 2- D.Dideban, "**Novel nano-CMOS modeling and simulation**", Final report of research project funded by nanotechnology research centre of the university of Kashan, Iran, Nov.2006.
- 3- D.Dideban, "**Operational Transconductance Amplifier Linearization Techniques in Nano CMOS technology**", Final report of research project funded by vice chancellor of research affairs, University of Kashan, Iran, Feb 2015.