Hossein Karimian

Assistant Professor

College: Faculty of Electrical and Computer Engineering

Department: Electrical Engineering - Electronics

دکتر حسین کریمیان

برنامه کلاسی و حضور هفتگی – نیمسال دوم سال تحصیلی ۱۴۰۲-۱۴۰۳

	۸ -- ۱۰	۱۰ -- ۱۲	۱۲:۳۰ -- ۱۴	۱۴ -- ۱۶	۱۶ -- ۱۸	۱۸ -- ۲۰
شنبه	حضور در صنعت					
یکشنبه		طراحی مدارهای VLSI		سیستم‌های دیجیتال ۲	طراحی سیستم‌های دیجیتال (ASIC و FPGA)	
دوشنبه	مراجعه دانشجویان	طراحی مدارهای VLSI	الکترونیک آنالوگ	مراجعه دانشجویان (با وقت قبلی)	مراجعه دانشجویان (با وقت قبلی)	طراحی سیستم‌های دیجیتال (ASIC و FPGA)
سه شنبه	مراجعه دانشجویان	مراجعه دانشجویان	الکترونیک آنالوگ	سیستم‌های دیجیتال ۲		

در صورت کار ضروری و یا جهت تعیین وقت با شماره داخلی ۳۴۷۵ تماس گرفته یا به آدرس زیر ایمیل ارسال کنید:

hkarimiyan@kashanu.ac.ir

Electrical & Computer Department
University of Kashan

Papers in Conferences

1. Zahra Khojasteh and Hossein Karimiyan ,“Design and optimization of aging and process variation sensor” ,24th National computer conference ,2018.
2. Ali Ghomi, Hossein Karimiyan ,“Smart image sensor design for hardware processing window implementation targeted for IOT application” ,24th National computer conference ,2018.
3. Hossein Karimiyan ,,”Design, analysis, and testing of interface board for navigation system with self-test capability” ,International navigation conference ,2016.
4. Abbas. Mahbod, Hossein Karimiyan ," A Low Power, High Fill Factor Smart CMOS Image Sensor for Internet-of-Things Based Systems” ,10th Conference on Machine Vision and Image Processing ,2017.
5. Ali Seif Kashani, Hossein Karimiyan ,“Design and characterization of all-graphene logical configurable block for FPGA application” ,21st National computer conference ,2016.
6. Abbas. Mahbod, Hossein Karimiyan ,"A High Fill Factor, Power-Gated Smart Image Sensor with a

Novel Efficient Simulation Methodology” ,th International conference on Applied Research in Computer Eng, and Processing, ,2016.
7. Abbas. Mahbod, Hossein Karimiyan ,"Power reduction in Smart Image Sensor with detect and track capability” ,9th Machine vision and image processing ,2016.
8. Alireza Saadatzade, Hossein Karimiyan ,“Soft error resilient static latch with low delay and low power consumption” ,2nd International avionic conference ,2013.
9. Hossein Karimiyan ,“Design, analysis, and test of central processing board with high reliability” ,2nd International avionic conference ,2013.
10. Alireza Saadatzade, Hossein Karimiyan ,“Reliability analysis of aero-space systems against sub-atomic particle hit” ,2nd International avionic conference ,2013.
11. "On-Chip NBTI and PBTI Tracking through an All-Digital Aging Monitor Architecture." ,Power and Timing Modeling, Optimization and Simulation (PATMOS) 2012 ,2012.

Papers in Journals

1. Mehnoush Arjmandian , Hossein Karamitaher , Hossein Karimiyan Alidash,Zigzag graphene nanoribbon antidot lattice for local interconnect applications: a precise computational method,Journal of Computational Electronics,2022 02 05.
2. Amir Ghadiyani ,& Hossein Karimiyan Alidash,New Approach for Designing and Optimally Selecting the Parameters of Graphene nano-ribbon Transistors in the Presence of Process Variation,ECS Journal of Solid State Science and Technology,Vol. 9,pp. 121012,2020 12 23.
3. Hadi Shirvani Filabadi ,& Hossein Karimiyan Alidash,Graphene Nanoribbon Field-Effect Transistors-Based Digital General-Purpose Input/Output Block,ECS Journal of Solid State Science and Technology,2020.
4. Sayed Ali Seif Kashani , Hossein Karimiyan Alidash , Hadi Shirvani Filabadi,All-Graphene Nano-Ribbon FET Based Complete FPGA Design,ECS Journal of Solid State Science and Technology,2020.
5. S. A. Seif Kashani, H. Karimiyan Alidash and S. Miryala,"Schottky-barrier graphene nanoribbon field-effect transistors-based field-programmable gate array's configurable logic block and routing switch",IET Circuits, Devices & Systems,2017.
6. S. A. Seif Kashani, H. Karimiyan Alidash and S. Miryala,"Design and Characterization of Graphene Nano-Ribbon Based D-Flip-Flop",Journal of Nanoelectronics and Optoelectronics,2017.
7. Abbas Mahbod, Hossein Karimiyan,"Ultralow power, high fill factor smart complementary metal oxide semiconductor image sensor with motion detection capability",J. Electron. Imaging,2016.
8. H. K. Alidash, A. Calimera, A. Macii, E. Macii and M. Poncino,"On-chip process variation-tracking through an all-digital monitoring architecture",IET Circuits, Devices & Systems,2012.
9. Abbas Mahbod, Hossein Karimiyan,"Power reduction, high fill factor smart in smart image sensor design with motion detection capability,",J. Machine Vision and Image Processing,2016.
10. A. Saadatzade, H. Karimiyan,“Soft error resilient static latch with low delay and low power consumption”,Soft Computing Journal,2013.
11. حمیدرضا شیرین کار,حسین کریمیان علیداش,An Optimization Algorithm for Dimensional Design of Graphene Nano-ribbon Field Effect Transistors for All-Graphene SRAM Chip,0000,مجله علمی محاسبات نرم 00 00,ISC.